

## HIGH PERFORMANCE MEMORY SYSTEMS%0A

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Design and Implementation of High-Performance Memory Systems for Future Packet Buffers Jorge Garc a, Jes us Corbal,Llorenc Cerd a, and Mateo Valero

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Delivering high capacity and speed with low power is the goal of next-gen memory and storage. Innovations in DDR5, GDDR6 and HBM2E will be critical to enable the demanding AI workloads of tomorrow in the data center. Micron delivers GDDR6 production performance on the **High-Performance Memories for Embedded Systems** Cypress offers our customers the highest performance and reliability NOR Flash, NAND Flash, SRAM, nvSRAM and F-RAM with discrete memory densities ranging from 4K-bit to 1G-bit.

#### **HPL - A Portable Implementation of the High-Performance ...**

HPL is a software package that solves a (random) dense linear system in double precision (64 bits) arithmetic on distributed-memory computers. It can thus be regarded as a portable as well as freely available implementation of the High Performance Computing Linpack Benchmark.

#### **High Bandwidth Memory - Wikipedia**

High Bandwidth Memory (HBM) is a high-performance RAM interface for 3D-stacked DRAM from Samsung, AMD and Hynix. It is to be used in conjunction with high-performance graphics accelerators and network devices.

The first devices to use HBM are the AMD Fiji GPUs.